

What Is Claimed Is:

1. A television tuner comprising:
 - a tuning circuit which tunes with a television signal
5 within a prescribed receiving frequency band;
 - a high frequency amplifier which amplifies the television
signal;
 - a mixer which converts the television signal into an
intermediate frequency signal; and
 - 10 an oscillating section which supplies a local oscillation
signal to the mixer, wherein
 - the tuning circuit is made up of an inductor and plural
capacitors;
 - wherein at least one of the capacitors is selected and
15 connected in parallel with the inductor;
 - wherein the oscillating section is made up of at least
plural oscillators which are included in an integrated circuit;
 - wherein in the integrated circuit, each of the
oscillators has a variable capacitor to vary a frequency of the
20 local oscillation signal;
 - wherein groups into which the receiving frequency band
is divided correspond to the oscillators on the basis of one
oscillator per group; and
 - wherein each of the oscillators generates local
25 oscillation frequencies depending on television signals in the
corresponding group.
2. The television tuner according to Claim 1, wherein

the integrated circuit includes the plural capacitors and switches to select the capacitors;

wherein the inductor is located outside the integrated circuit; and

5 wherein the integrated circuit has a terminal to connect the inductor with the capacitors.

3. The television tuner according to Claim 2, wherein the capacitors comprises a primary capacitor connected with the inductor and plural secondary capacitors which are selected by
10 the switches and connected with the inductor.

4. The television tuner according to Claim 3, wherein a capacitance value of each of a second to Nth ones of the secondary capacitors is $(2N-1)$ times as large as a capacitance value of a first one of the secondary capacitors.

15 5. The television tuner according to Claim 2, wherein the high frequency amplifier and the mixer are included in the integrated circuit.

6. The television tuner according to Claim 1, wherein the tuning circuit comprises an input tuning circuit and
20 an interstage tuning circuit;

wherein the input tuning circuit is located before the high frequency amplifier; and

wherein the interstage tuning circuit is located between the high frequency amplifier and the mixer.

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